

ADITYA INSTITUTE OF TECHNOLOGY AND MANAGEMENT

(AN AUTONOMOUS INSTITUTION)

EXAMINATION BRANCH

I M. TECH - I SEMESTER – (AR13 REGULATION) 2013 BATCH (AUTONOMOUS)

II MID EXAMINATIONS, JANUARY-2014

TIME TABLE

DATE: 28-12-2013

Examination Timings 01.40 PM to 03.40 PM

Date & Day of Examination	TE	DECS	IT	PEED	VLSI System Design	CSE
06-01-2014 (MONDAY)	Optimization Techniques & Applications	Digital System Design	Advanced Data Structures	Electrical Machine Modeling & Analysis	Digital System Design	Data Structures & Algorithms
07-01-2014 (TUESDAY)	Advanced Thermodynamics	VLSI Technology & Design	Advanced Unix Programming Data Structures	Analysis of Power Electronic Converters	VLSI Technology & Design	Computer Organization and Architecture
08-01-2014 (WEDNESDAY)	Advanced Heat & Mass Transfer	Analog & Digital IC Design	Data Base Management Systems	Power Electronic Control of DC Drives	Analog & Digital IC Design	Database Management Systems
09-01-2014 (THURSDAY)	Advanced Fluid Mechanics	Detection and Estimation of Signals	Advanced Computer Network	Micro Controllers & Applications	VHDL Modeling of Digital Systems	Software Engineering
10-01-2014 (FRIDAY)	Solar Energy Technology	Embedded & Real Time Systems	Code Optimization	Modern Control Theory	Embedded & Real Time Systems	Operating Systems
11-01-2014 (SATURDAY)	Non Conventional Energy Sources	Advanced Digital Signal Processing	Object Oriented Software Engineering	Design & Monitoring of Machines	Electronic Design Automation Tools	Object Oriented Programming

**Copy to: Director,
 Controller of the Examinations,
 HODs,
 Officer I/c, Examinations,
 Dy. Director (Diploma)
 Estate Officer,
 In-charge, Transport**

**Sd/-
PRINCIPAL**